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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	Filed Herewith 10/799 237		
		Filing Date	March 11, 2004		
		First Named Inventor:	JengPing Lu		
		Examiner name: unknown E. LESTER	GROUP: unknown 2873		
Sheet	1	of	1	Attorney Docket Number	A3490 (XCP-040)

U.S. PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ^{2/(f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear
EPH	A01	US-3,842,189	10/15/1974	Southgate	
EPH	A02	US-5,613,861	03/25/1997	Smith et al.	
		US-			
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		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Cntry Code ³ Number ⁴ Kind Code ^{5/(f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EPH	Fonash: "Integration Of Thin Film Electronics And MEMs", SPIE Vol. 2722(2/96), pp. 66-74.
EPH	Martin et al.: "High-Voltage Amorphous Silicon Thin-Film Transistors", IEEE Transactions On Electron Devices, Vol. 40, No. 3, March 1993, pp. 634-643.
EPH	Huang et al.: "Device Sensitivity Of Field-Plated Polysilicon High-Voltage TFT's And Their Application To Low-Voltage Operation", IEEE Electron Device Letters, Vol. 11, November 1990, pp. 541-543.
EPH	Welsh et al.: "NCAP Displays: Optical Switching And Dielectric Properties", SID 90 Digest, 1990, pp. 220-223.
EPH	Chuang et al.: "Page-Wide High-Voltage Polysilicon TFT Array For Electronic Printing", SID 90 Digest, 1990, pp. 508-511.

EXAMINER SIGNATURE: *E. Lester*

DATE CONSIDERED: 12-22-04

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